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FILED UNDER 35 U.S.C. 371

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM 10031672	FILING DATE 09/09/2002	CLASS 326	SUBCLASS 10	GAU 2819	EXAMINER J. Cho
**APPLICANTS: Athas William; Tzartzanis Nestor; Mao Weihua; Peterson Lena; OK-gc					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/US00/19608 07/18/2000 which claims benefit of 60/144,520 07/19/1999 and claims benefit of 60/174,509 01/05/2000 OK-gc					
** FOREIGN APPLICATIONS VERIFIED: Done-gc					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials gc				ATTORNEY DOCKET NO 18036-24199	
TITLE : High-performance clock-powered logic					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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